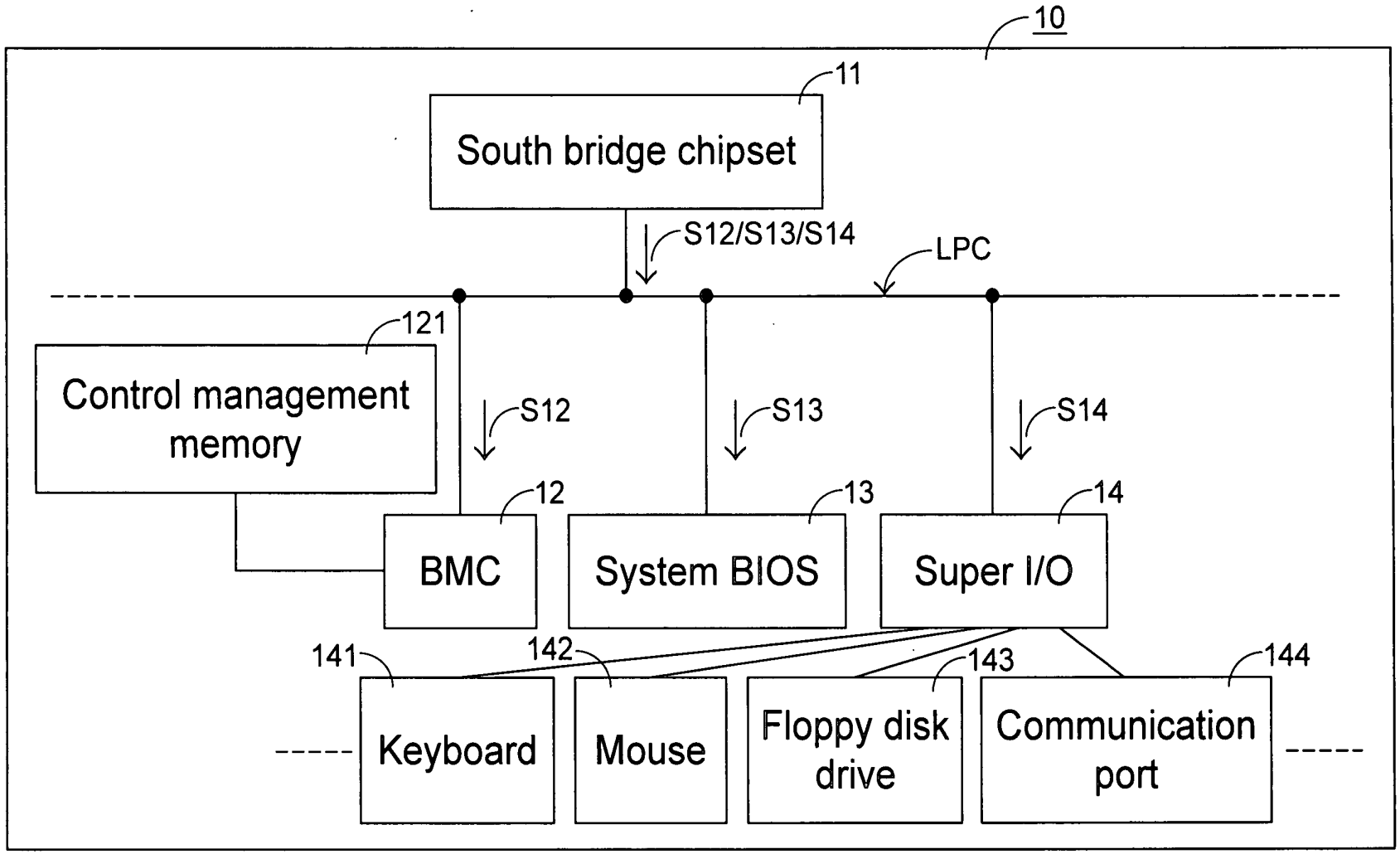
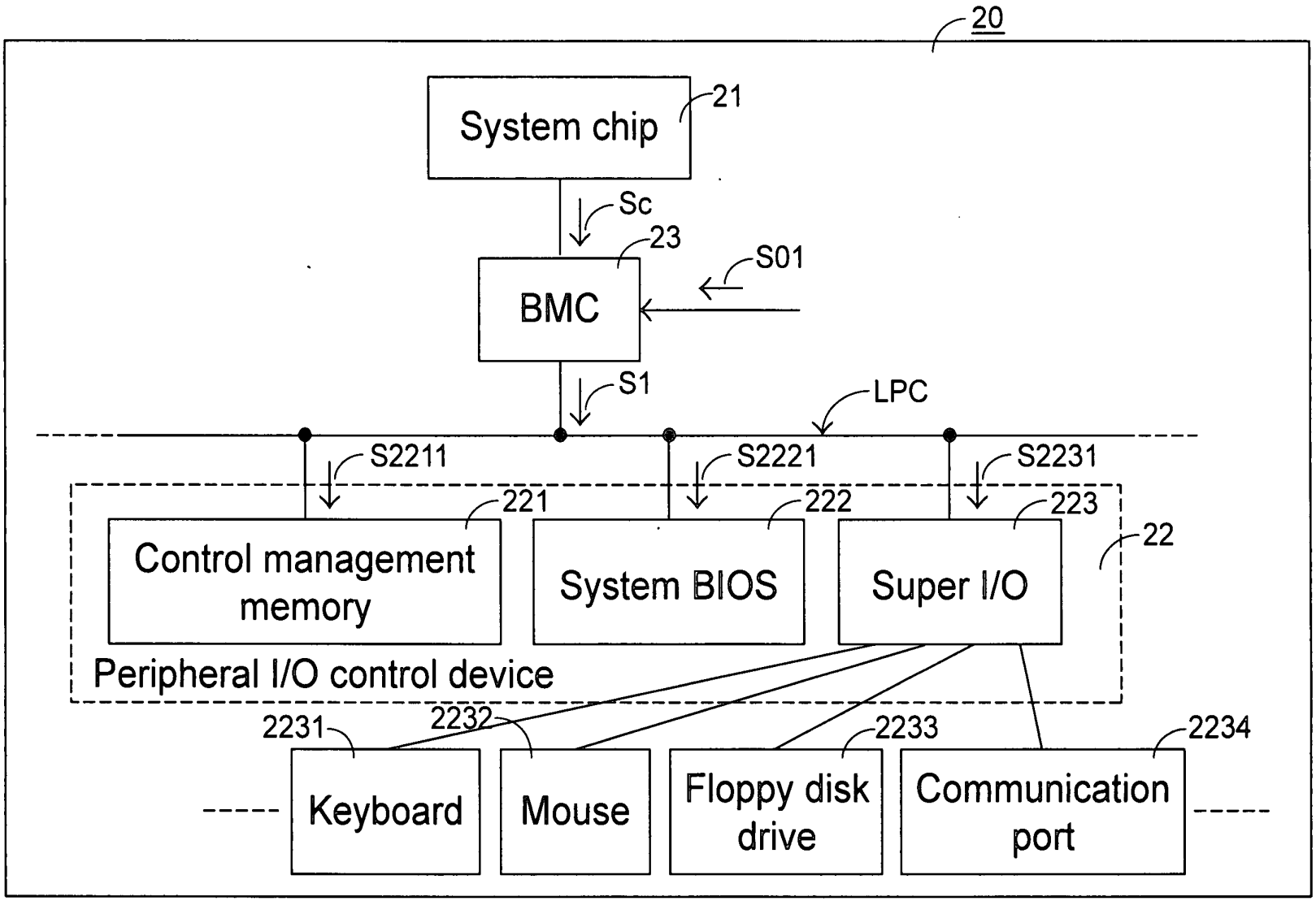


Fig.1
PRIOR ART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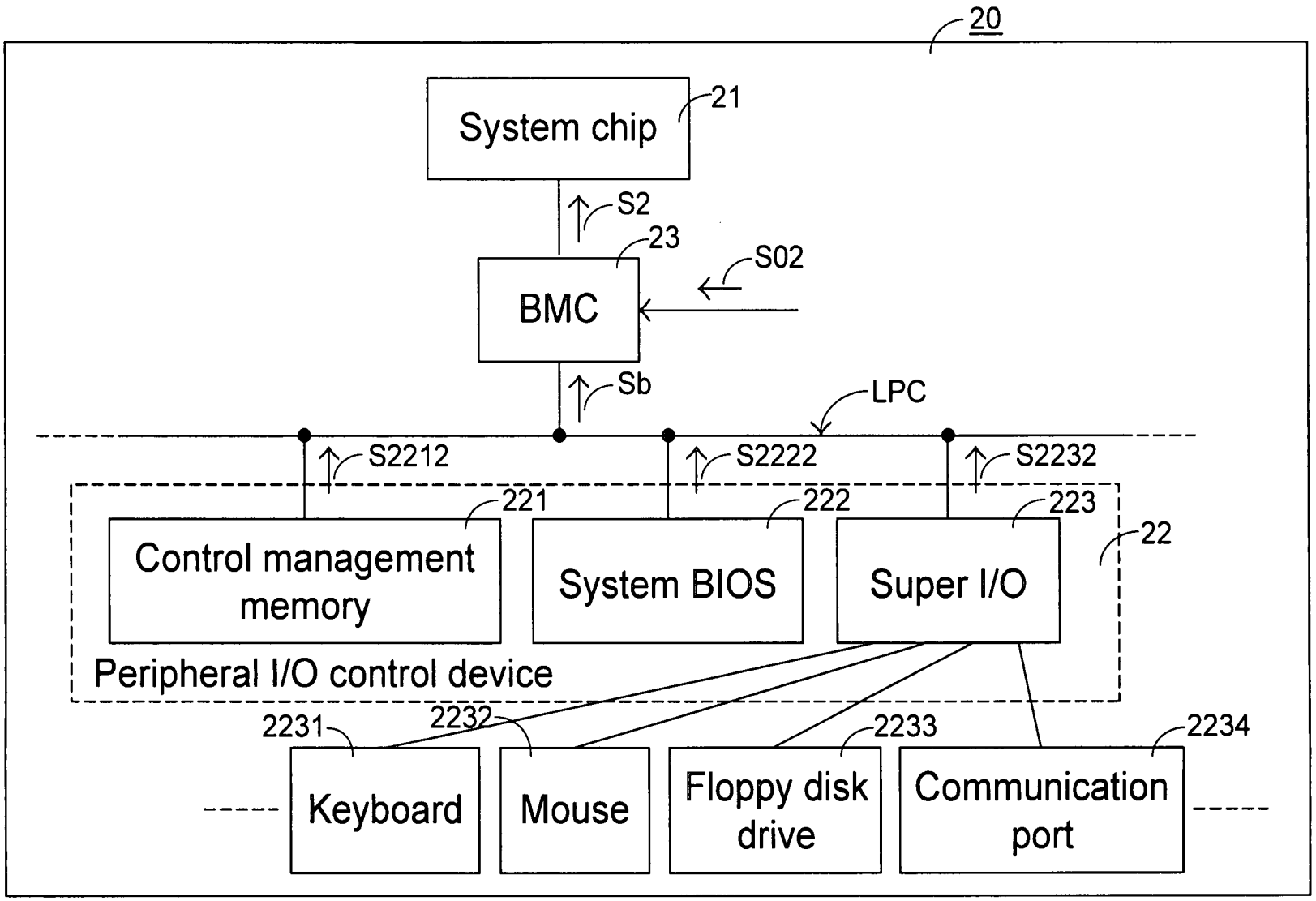
System chip
21
Sc
23
S01
BMC
S1
LPC
S2211
S2221
S2231
221
222
223
22
Control management memory
System BIOS
Super I/O
Peripheral I/O control device
2231
2232
2233
2234
Keyboard
Mouse
Floppy disk drive
Communication port

Fig.2A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System chip
21
S2
23
S02
BMC
Sb
LPC
S2212
S2222
S2232
221
222
223
22
Control management memory
System BIOS
Super I/O
Peripheral I/O control device
2231
2232
2233
2234
Keyboard
Mouse
Floppy disk drive
Communication port

Fig.2B